

REMARKS

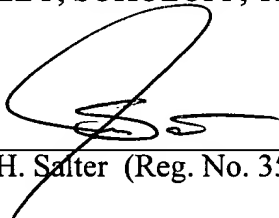
Applicants respectfully request consideration of the subject application as amended herein.

If there are any additional charges, please charge them to our Deposit Account No. 02-2666. If a telephone conference would facilitate the prosecution of this application, the Examiner is invited to contact James H. Salter at (408) 720-8300.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 6/14/, 2002


James H. Salter (Reg. No. 35,668)

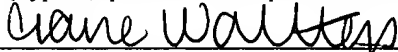
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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to the Assistant Commissioner for Patents, Washington, D. C. 20231 on June 14, 2002

(Date of Deposit)

Claire Walters

(Typed or printed name of person mailing correspondence)



(Signature of person mailing correspondence)

Marked Up Changes

[0003] The request stream from each different initiator can be described as a thread. If a DRAM scheduler does not re-order requests from the same thread, intra-thread request order is maintained, and the overall DRAM request order is simply an interleaving of the sequential per-thread request streams. This is the definition of Sequential Consistency, the strongest memory ordering model available for systems that include multiple initiator components. [[(For further discussion regarding Sequential Consistency, see [L.] Leslie Lamport[], How to Make a [Multi-processing] Multiprocessor Computer That Correctly Executes Multiprocess Programs[], IEEE Transaction [o]n Computers, [C-28(9):241-248,] Vol. C-28, No. 9, September 1979[], pgs. 690-691.)